

PK(PD)70FG80/160

UL: E76102

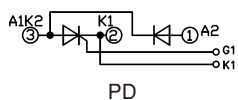
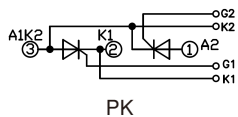
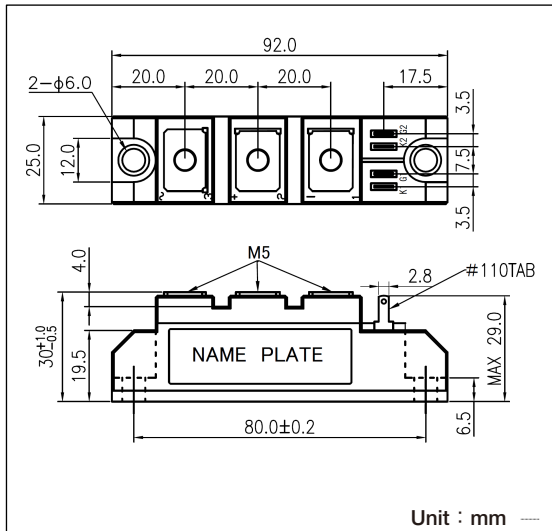
《Features》

Power Thyristor/Diode Module PK70FG series are designed for various rectifier circuits and power controls. For your circuit application, following internal connections and wide voltage ratings up to 1600V are available. And electrically isolated mounting base make your mechanical design easy.

- $I_{T(AV)}$ 70A, $I_{T(RMS)}$ 110A, I_{TSM} 1600A
- di/dt 100A/ μ s
- dv/dt 1000V/ μ s

《Applications》

- Various rectifiers / AC/DC motor drives / Heater controls / Light dimmers / Static switches



■ Maximum Ratings (T_j=25°C unless otherwise specified)

Item	Symbol	Unit	PK70FG80	PK70FG160
			PD70FG80	PD70FG160
*Repetitive Peak Reverse Voltage	V_{RRM}	V	800	1600
*Non-Repetitive Peak Reverse Voltage	V_{RSM}	V	960	1700
Repetitive Peak Off-state Voltage	V_{DRM}	V	800	1600

Item	Symbol	Unit	Ratings	Conditions
*Average On-state Current	$I_{T(AV)}$	A	70	Single phase, half wave 180° conduction, T _c =84°C
*R.M.S. On-state Current	$I_{T(RMS)}$	A	110	Single phase, half wave 180° conduction, T _c =84°C
*Surge On-state Current	I_{TSM}	A	1460/1600	1/2 cycle, 50Hz/60Hz, Peak value, non-repetitive
*I ² t(for fusing)	I ² t	A ² s	10660	Value for one cycle surge current
Peak Gate Power Dissipation	P_{GM}	W	10	
Average Gate Power Dissipation	$P_{G(AV)}$	W	1	
Peak Gate Current	I_{FGM}	A	3	
Peak Gate Voltage(Forward)	V_{FGM}	V	10	
Peak Gate Voltage(Reverse)	V_{RGM}	V	5	
Critical Rate of Rise of On-state Current	di/dt	A/ μ s	100	$I_G=100mA$, $V_D=1/2V_{DRM}$, $dI_G/dt=0.1A/\mu s$
*Isolation Breakdown Voltage	V_{ISO}	V	2500	A.C. 1minute
*Operating Junction Temperature	T _j	°C	-40 to +125	
*Storage Temperature	T _{STG}	°C	-40 to +125	
Mounting Torque	Mounting M5	N·m (kgf·cm)	2.7(28)	Recommended Value 1.5 to 2.5 (15 to 25)
	Terminal M5		2.7(28)	Recommended Value 1.5 to 2.5 (15 to 25)
Mass		g	170	Typical value

■ Electrical Characteristics (T_j=25°C unless otherwise specified)

Item	Symbol	Unit	Ratings	Conditions
Repetitive Peak Off-state Current,max.	I_{DRM}	mA	20	at V_{DRM} , Single phase, half wave T _j =125°C
*Repetitive Peak Reverse Current,max.	I_{RRM}	mA	20	at V_{DRM} , Single phase, half wave T _j =125°C
*Peak On-state Voltage,max.	V_{TM}	V	1.6	On-State Current 210A, Inst.Measurement
Gate Trigger Current/Voltage, max.	I_{GT}/V_{GT}	mA	50/3	$I_T=1A, V_D=6V$
Non-Trigger Gate Trigger Voltage,min.	V_{GD}	V	0.25	T _j =125°C, $V_D=1/2V_{DRM}$
Turn-on Time,max	t _{gt}	μ s	10	$I_T=70A$, $I_G=100mA$, T _j =25°C, $V_D=1/2V_{DRM}$, $dI_G/dt=0.1A/\mu s$
Critical Rate of Rise of Off-state Voltage,min.	dv/dt	V/ μ s	1000	T _j =125°C, $V_D=2/3V_{DRM}$, Exponential wave
Holding Current	I_H	mA	50	T _j =25°C
Latching Current	I_L	mA	100	T _j =25°C
Thermal Resistance,max	R_{th}	°C/W	0.37	Junction to case

*mark : Thyristor and Diode part. No mark : Thyristor part

