

TRIAC(Through Hole)

TMG1D60 5

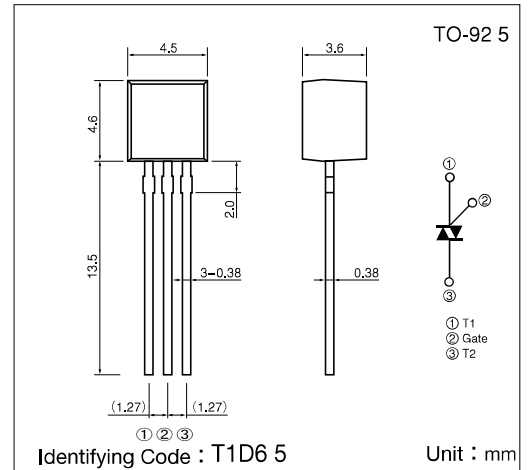
SanRex Triac TMG1D60 5 is designed for full wave AC control applications. It can be used as an ON/OFF function or for phase control operation.

Typical Applications

- Home Appliances : Washing Machines, Vacuum Cleaners, Rice Cookers, Micro Wave Ovens, Hair Dryers, other control applications
- Industrial Use : SMPS, Copier Machines, Motor Controls, Dimmer, SSR, Heater Controls, Vending Machines, other control applications

Features

- $I_{T(RMS)}=1A$
- High Surge Current



Maximum Ratings

(Unless otherwise $T_j=25^\circ C$)

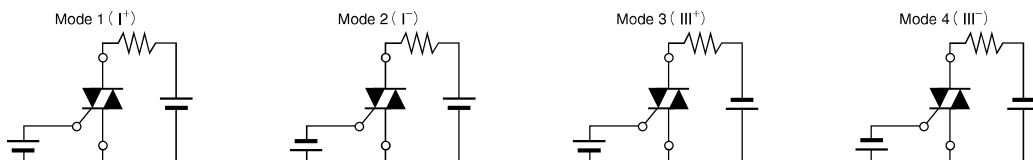
Symbol	Item	Reference	Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage		600	V
$I_{T(RMS)}$	R.M.S. On-State Current	$T_c=52^\circ C$	1	A
I_{TSM}	Surge On-State Current	50/60Hz, One cycle, Peak value, non-repetitive	8/8.8	A
I^2t	I^2t (for fusing)		0.32	A^2s
P_{GM}	Peak Gate Power Dissipation		1	W
$P_{G(AV)}$	Average Gate Power Dissipation		0.1	W
I_{GM}	Peak Gate Current		0.5	A
V_{GM}	Peak Gate Voltage		6	V
T_j	Operating Junction Temperature		$-40 \sim +125$	$^\circ C$
T_{stg}	Storage Temperature		$-40 \sim +150$	$^\circ C$
	Mass		0.2	g

Electrical Characteristics

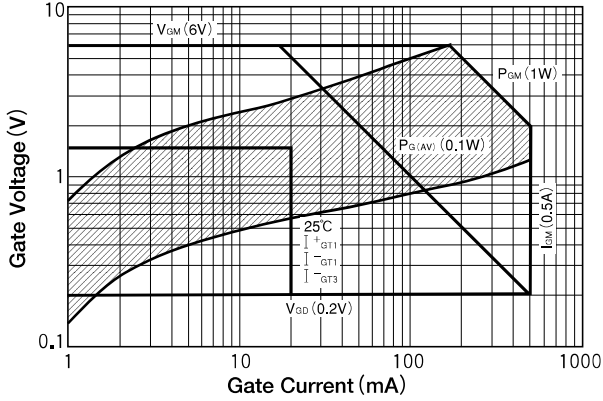
(Unless otherwise $T_j=25^\circ C$)

Symbol	Item	Reference	Ratings			Unit
			Min.	Typ.	Max.	
I_{DRM}	Repetitive Peak Off-State Current	$V_D=V_{DRM}$, $T_j=125^\circ C$, Single phase, half wave			0.5	mA
V_{TM}	Peak On-State Voltage	$I_T=1.5A$, Inst. measurement			1.75	V
I_{GT1}^+	Gate Trigger Current	$V_D=6V$, $R_L=10\Omega$			20	mA
I_{GT1}^-			2		20	
I_{GT3}^+			3		-	
I_{GT3}^-			4		20	
V_{GT1}^+	Gate Trigger Voltage				1.5	V
V_{GT1}^-			2		1.5	
V_{GT3}^+			3		-	
V_{GT3}^-			4		1.5	
V_{GD}	Non-Trigger Gate Voltage	$V_D=1/2 V_{DRM}$, $T_j=125^\circ C$	0.2			V
$(dv/dt)_c$	Critical Rate of Rise of Off-State Voltage at Commutation	$(di/dt)_c=-0.5A/ms$, $V_D=2/3 V_{DRM}$, $T_j=125^\circ C$	4			$V/\mu s$
I_H	Holding Current			5		mA
$R_{th(j-c)}$	Thermal Resistance	Junction to case			50	$^\circ C/W$

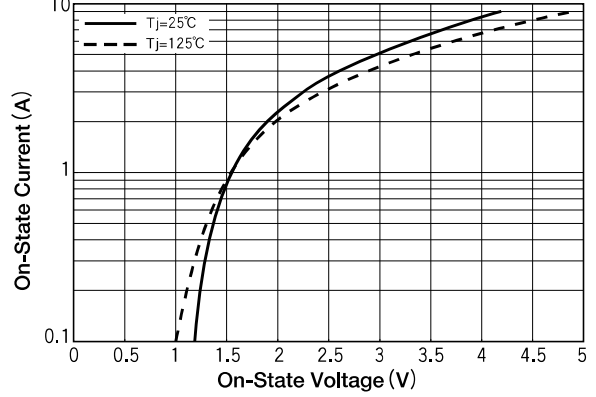
Trigger mode of the triac



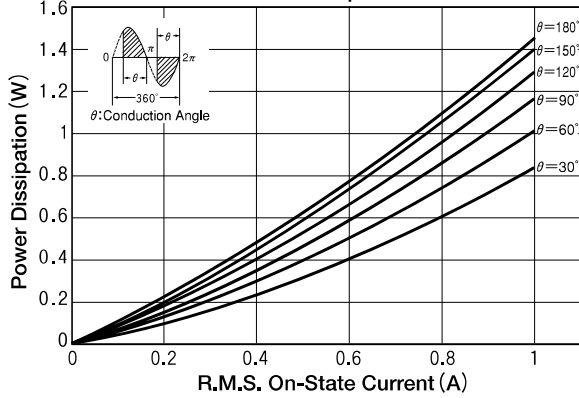
Gate Characteristics



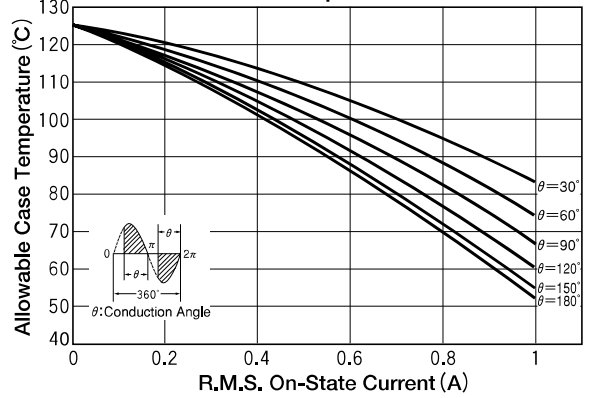
Maximum On-State Characteristics



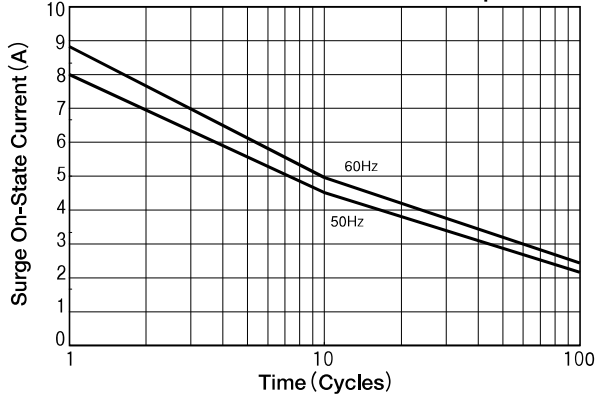
R.M.S. On-State Current vs Maximum Power Dissipation



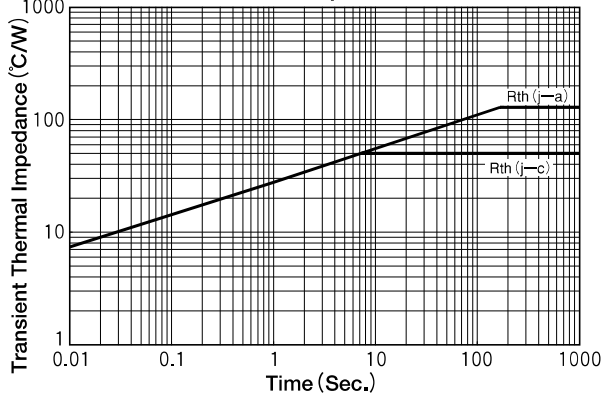
R.M.S. On-State vs Allowable Case Temperature



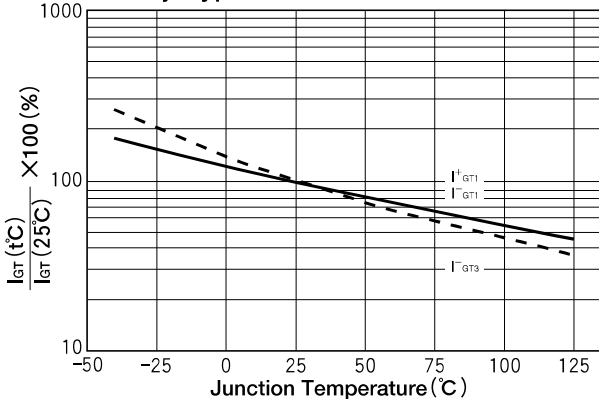
Surge On-State Current Rating (Non-Repetitive)



Transient Thermal Impedance



I_{GT} - T_j (Typical)



V_{GT} - T_j (Typical)

